

Linear Cmos Rf Power Amplifiers For Wireless Applications Efficiency Enhancement And Frequency Tunable Capability Og Circuits And Signal Processing

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The work establishes the design flow for the optimization of linear CMOS power amplifiers from the first steps of the design to the final IC implementation and tests. The authors also focuses on design guidelines of the inductor's geometrical characteristics for power applications and covers their measurement and characterization.

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Linear CMOS RF Power Amplifiers - media control

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Power amplifier classes. Power amplifier circuits (output stages) are classified as A, B, AB and C for linear designs—and class D and E for switching designs. The classes are based on the proportion of each input cycle (conduction angle) during which an amplifying device passes current. The image of the conduction angle derives from amplifying a sinusoidal signal.

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